Notice of Allowability	10/090,735  Examiner  Junghwa M. Im	FIGUEROA ET AL. Art Unit 2811
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The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS In herewith (or previously mailed), a Notice of Allowance (PTOL-8: NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.3:	S (OR REMAINS) CLOSED in 5) or other appropriate common RIGHTS. This application is 13 and MPEP 1308.	n this application. If not included unication will be mailed in due course. THIS
	<u>//25/2007</u> .	
2. The allowed claim(s) is/are 1.8 and 26-34.		
<ol> <li>Acknowledgment is made of a claim for foreign priority a)</li> <li>All b)</li> <li>Some* c)</li> <li>None of the:</li> <li>1.</li> <li>Certified copies of the priority documents had:</li> <li>Certified copies of the priority documents had:</li> <li>Copies of the certified copies of the priority documents had:</li> <li>International Bureau (PCT Rule 17.2(a)).</li> <li>* Certified copies not received:</li> </ol>	ve been received. ve been received in Application	on No
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	" of this communication to file IMENT of this application.	e a reply complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be sub INFORMAL PATENT APPLICATION (PTO-152) which gi	mitted. Note the attached EXA	AMINER'S AMENDMENT or NOTICE OF r declaration is deficient.
5. CORRECTED DRAWINGS ( as "replacement sheets") m	ust be submitted.	
(a) ☐ including changes required by the Notice of Draftspe		w ( PTO-948) attached
1)  hereto or 2)  to Paper No./Mail Date		
<ul><li>(b) ☐ including changes required by the attached Examine Paper No./Mail Date</li></ul>	r's Amendment / Comment or	r in the Office action of
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	1.84(c)) should be written on to the header according to 37 CF	he drawings in the front (not the back) of FR 1.121(d).
<ol> <li>DEPOSIT OF and/or INFORMATION about the dep attached Examiner's comment regarding REQUIREMENT</li> </ol>	osit of BIOLOGICAL MAT	ERIAL must be submitted. Note the
Attachment(s)	<b>-</b>	
1. Notice of References Cited (PTO-892)		formal Patent Application
<ol> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statements (PTO/SB/08),</li> </ol>	Paper No.,	ummary (PTO-413), /Mail Date Amendment/Comment
Paper No./Mail Date		
<ol> <li>Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ol>	8.	Junghwa Im Patent Examiner Art Unit: 2811

## **DETAILED ACTION**

## **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

Claim 1, A substrate to mount a die having at least one input signal terminal, the substrate keeping an impedance variation between an input signal entering the substrate from a receiving substrate and an output signal provided to the at least one input terminal below a predetermined value, the substrate comprising:

- a dielectric core member having an example thickness of 800 microns;
- a first plurality of dielectric lamination layers on a first side of the <u>dielectric</u> core member, each having an example thickness of 30 microns, and wherein the dielectric core member comprises material of different dielectric permittivity in comparison to a permittivity of material of the dielectric lamination layers;

a second plurality of conductive layers on the first side of the <u>dielectric</u> core member, each having an example thickness of 25 microns, and including at least one connector a plurality of ball grid array (BGA) connectors on a first surface of an uppermost one of the second plurality of conductive layers to couple to the at least one input signal terminal corresponding terminals of the die; and

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a single conductive layer on a second side of the <u>dielectric</u> core member, having an example thickness of 17 microns, wherein the single conductive layer comprises at least one land plurality of lands; and to couple to the input signal from the receiving substrate.

Claim 8, A system comprising: a die having a plurality of terminals, including at least one input signal terminal;

a receiving substrate having a plurality of terminals, including at least one terminal to provide an input signal;

a layered substrate including

a dielectric core member;

a first plurality of dielectric lamination layers on a first side of the <u>dielectric</u> core member, wherein the dielectric core member comprises material of different dielectric permittivity in comparison to a permittivity of material of <u>the first plurality of</u> the dielectric lamination layers;

a second plurality of conductive layers on the first side of the <u>dielectric</u> core member, including at least one connector on a first surface of an uppermost one of the second plurality of conductive layers, the connector being coupled to the at least one input signal terminal; and

a single conductive layer on a second side of the core member, wherein the single conductive layer comprises at least one land coupled to the input signal from the receiving substrate.

Claim 31, A substrate to mount a die having at least one input signal terminal, the

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substrate keeping an impedance variation between an input signal entering the substrate from a receiving substrate and an output signal provided to the at least one input terminal below a predetermined value, the substrate comprising:

a dielectric core member;

a first plurality of dielectric lamination layers on a first side of the <u>dielectric</u> core member, wherein the dielectric core member comprises material of different dielectric permittivity in comparison to a permittivity of material of <u>the first plurality of</u> dielectric lamination layers;

a second plurality of conductive layers on the first side of the <u>dielectric</u> core member, including at least one connector on a first surface of an uppermost one of the second plurality of conductive layers to couple to the at least one input signal terminal of the die; and

a single conductive layer on a second side of the core member, wherein the single conductive layer comprises at least one land to couple to the input signal from the receiving substrate.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Junghwa M. Im whose telephone number is (571) 272-1655. The examiner can normally be reached on MON.-FRI. 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne A. Gurley can be reached on (571) 272-1670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <a href="http://pair-direct.uspto.gov">http://pair-direct.uspto.gov</a>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Junghwa M. Im

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jmi